

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 07/14/03
Application Type:: Regular
Subject Matter:: Utility
Title:: Method for Gross I/O Functional
Test at Wafer Sort
Attorney Docket Number:: X-1269-1P US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 6
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: David
Family Name:: Mark
Street:: 1724 Briarbrush Court
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95131

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Yung-Cheng
Family Name:: Chen
Street:: 1115 Amur Creek Court
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95120

Applicant Authority Type:: Inventor
 Primary Citizenship Ctry:: US
 Status:: Full Capacity
 Given Name:: Randy
 Middle Name:: J.
 Family Name:: Simmons
 Street:: 1940 Huxley Court
 City:: San Jose
 State or Province:: CA
 Postal or Zip Code:: 95125

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Continuation In Part	10/355,563	1/31/03

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
 Street:: 2100 Logic Drive
 City:: San Jose
 State or Province:: California
 Postal or Zip Code:: 95124